

PEB-7710VLA-R2

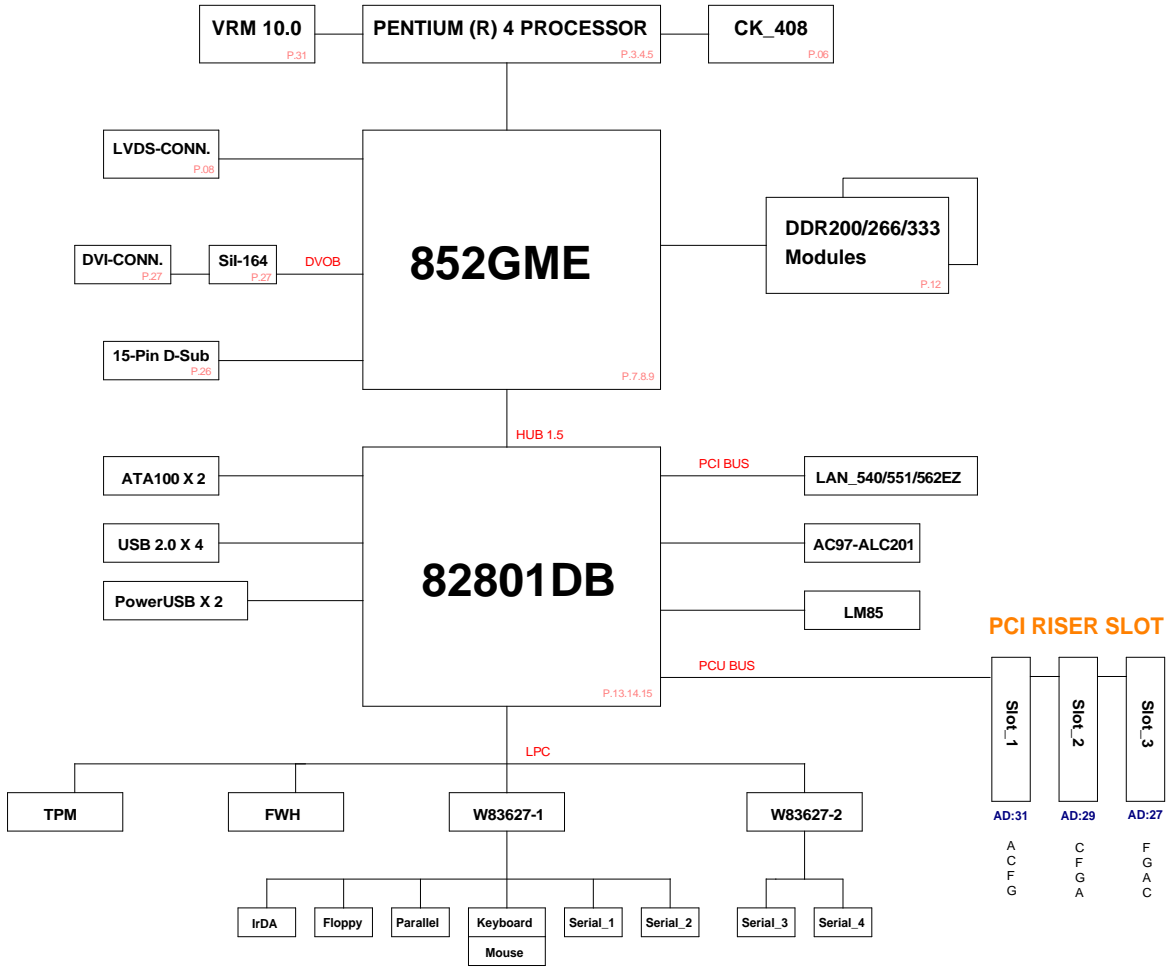
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<p>Naming Rule for Part Value :</p> <table> <tr> <td>/6 0603 Package size</td> <td>/1 1% resistor</td> </tr> <tr> <td>/8 0805 Package size</td> <td>/X Not stuffed</td> </tr> <tr> <td>/10 1210 Package size</td> <td></td> </tr> <tr> <td>/12 1206 Package size</td> <td></td> </tr> <tr> <td>/18 1812 Package size</td> <td></td> </tr> </table> <p><i>Note : Qualities of all Ceramic capacitors used in this schematic should be equal or better than X5R grade.</i></p>		/6 0603 Package size	/1 1% resistor	/8 0805 Package size	/X Not stuffed	/10 1210 Package size		/12 1206 Package size		/18 1812 Package size	
/6 0603 Package size	/1 1% resistor										
/8 0805 Package size	/X Not stuffed										
/10 1210 Package size											
/12 1206 Package size											
/18 1812 Package size											

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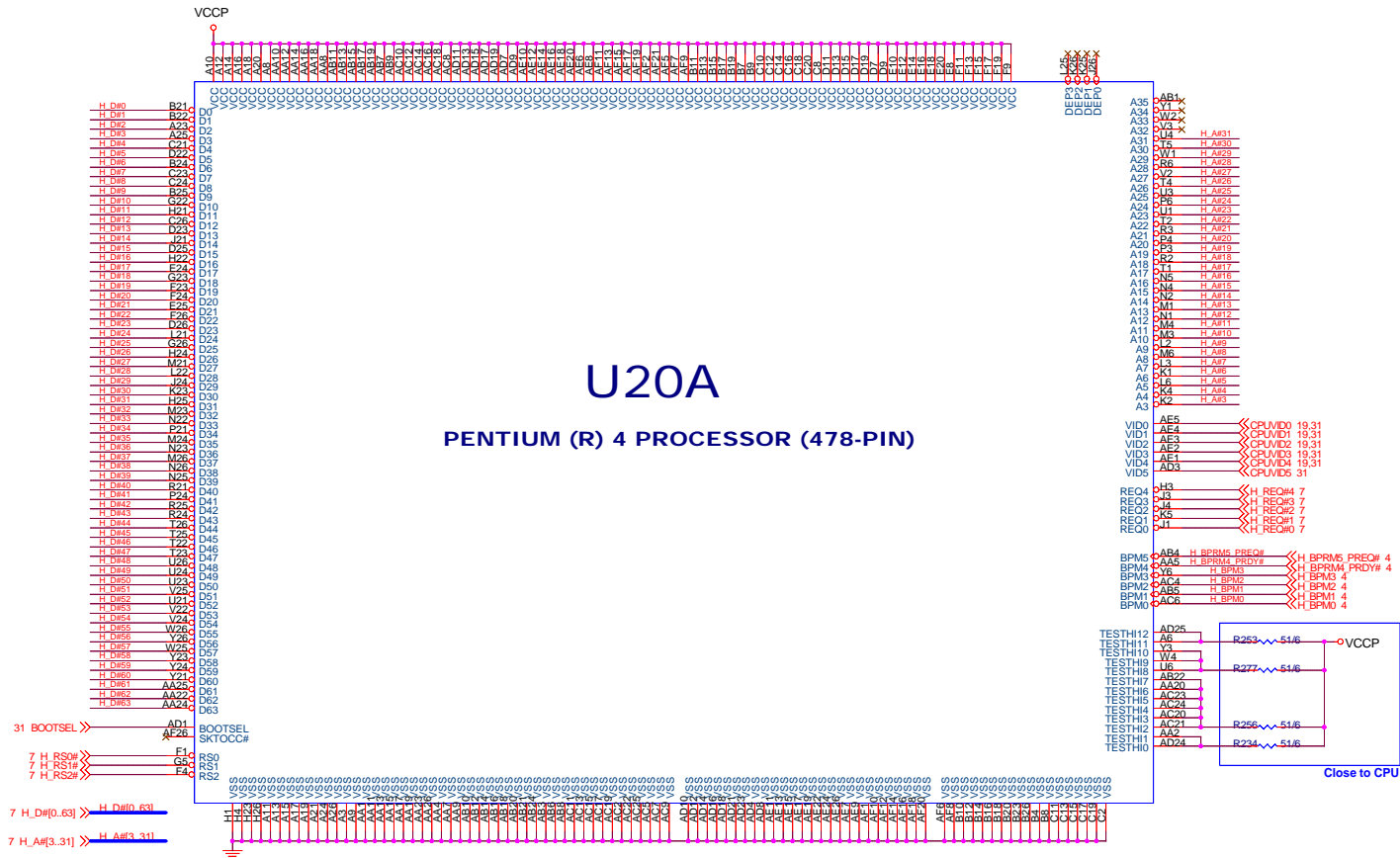
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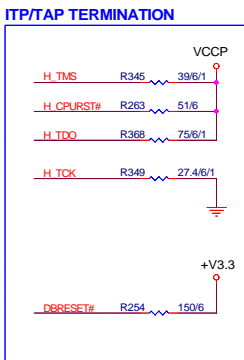
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BLOCK DIAGRAM



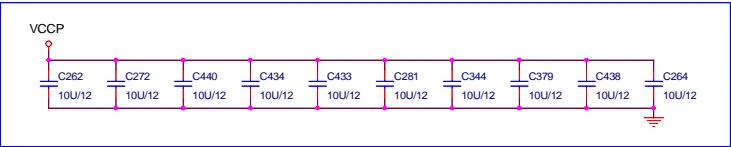
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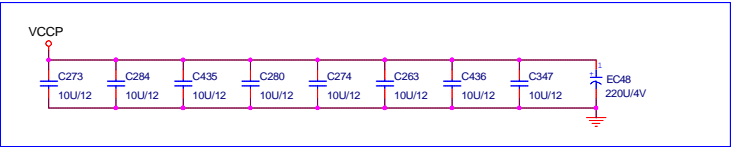


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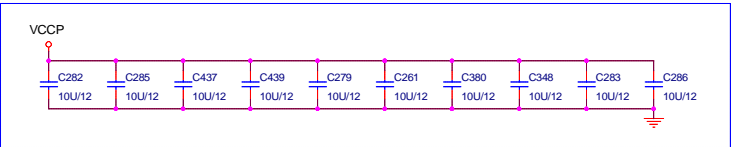
Put the cap on the North side of the processor



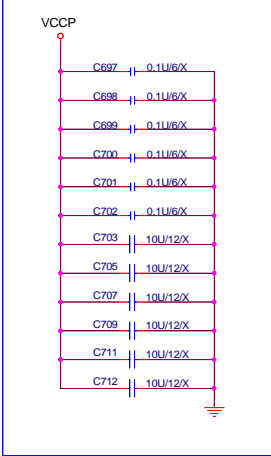
Put the cap in the processor cavity



Put the cap on the south side of the processor

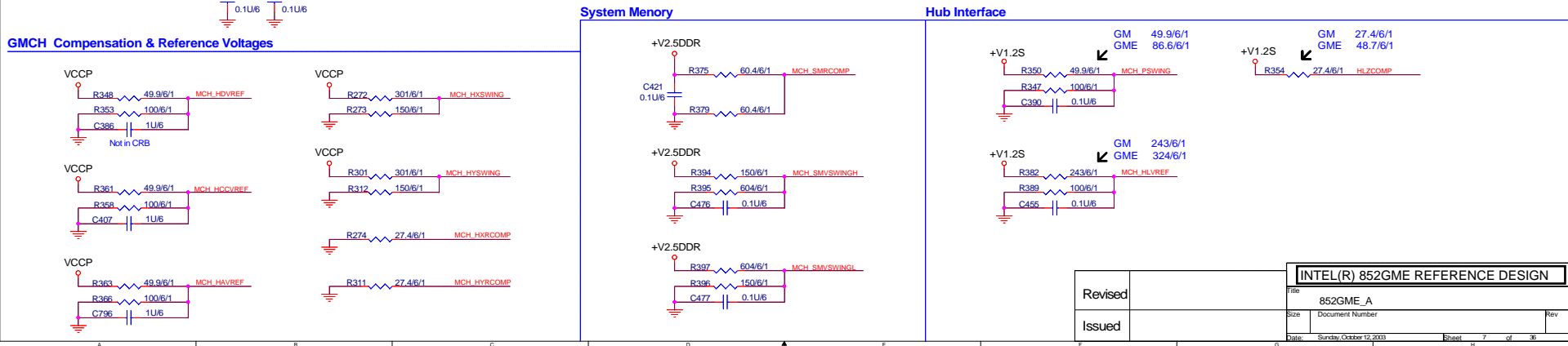
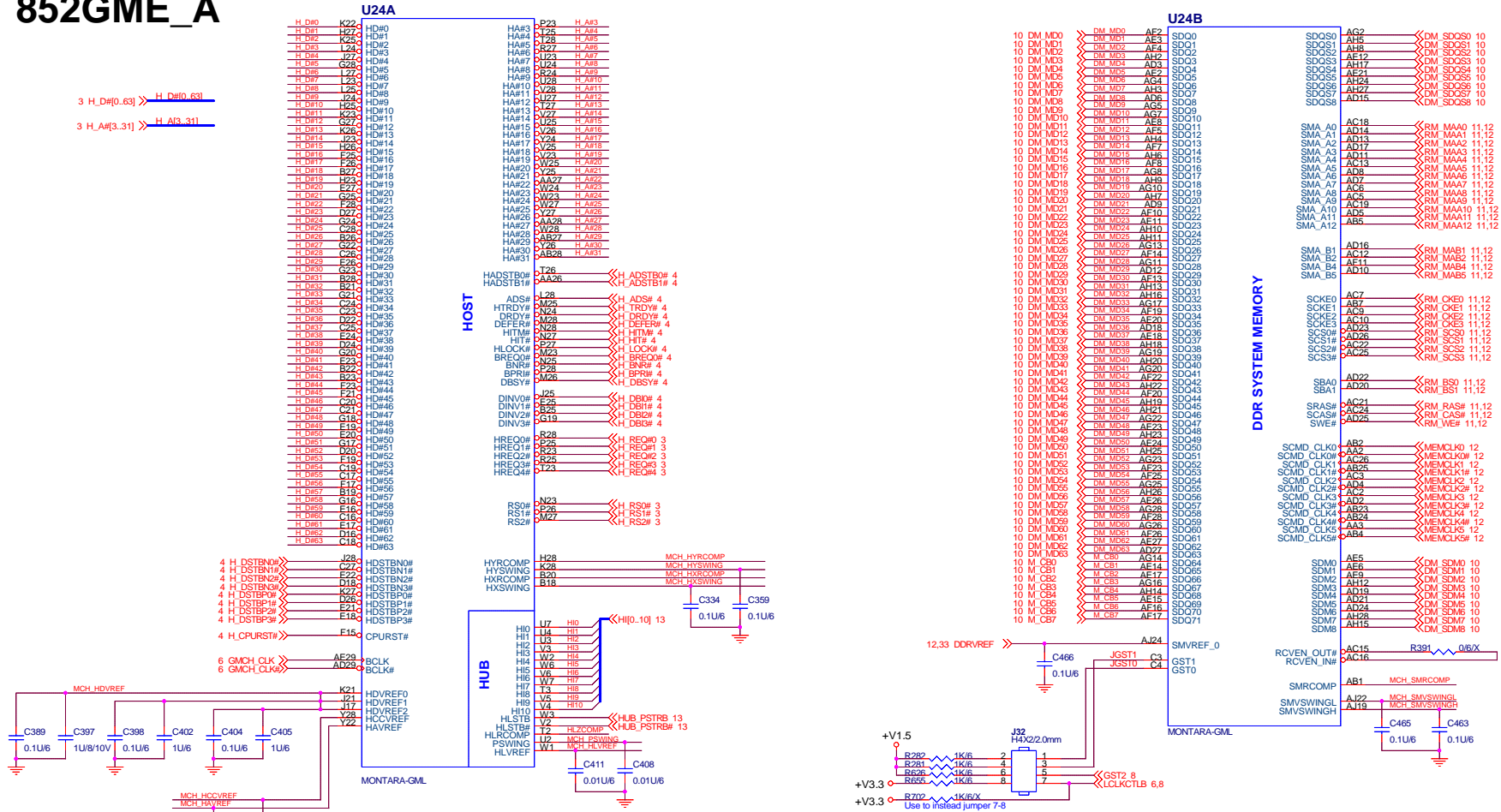


Reserve on CPU bottom side

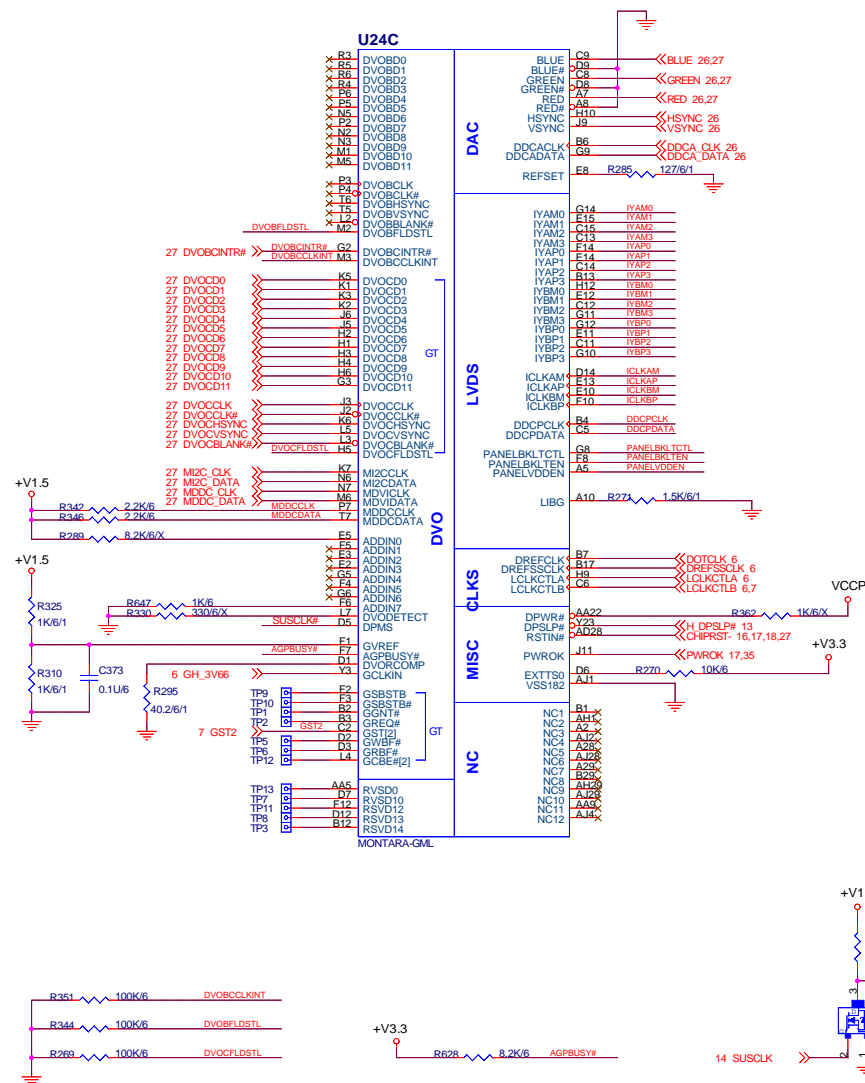


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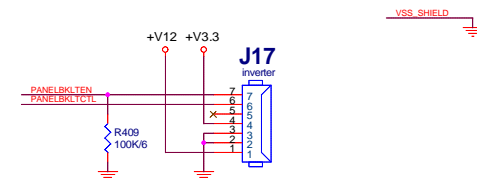
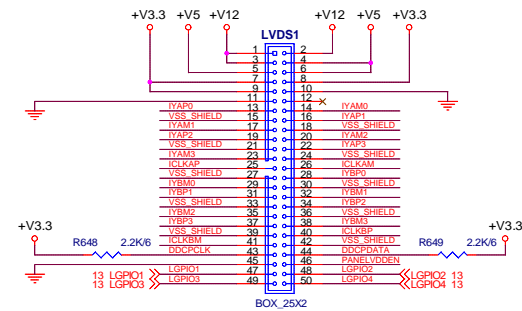
852GME_A



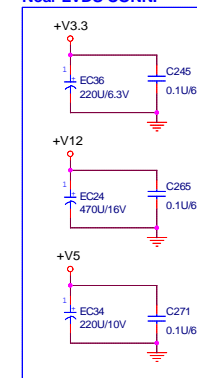
852GME_B



LVDS & Backlight Conn.

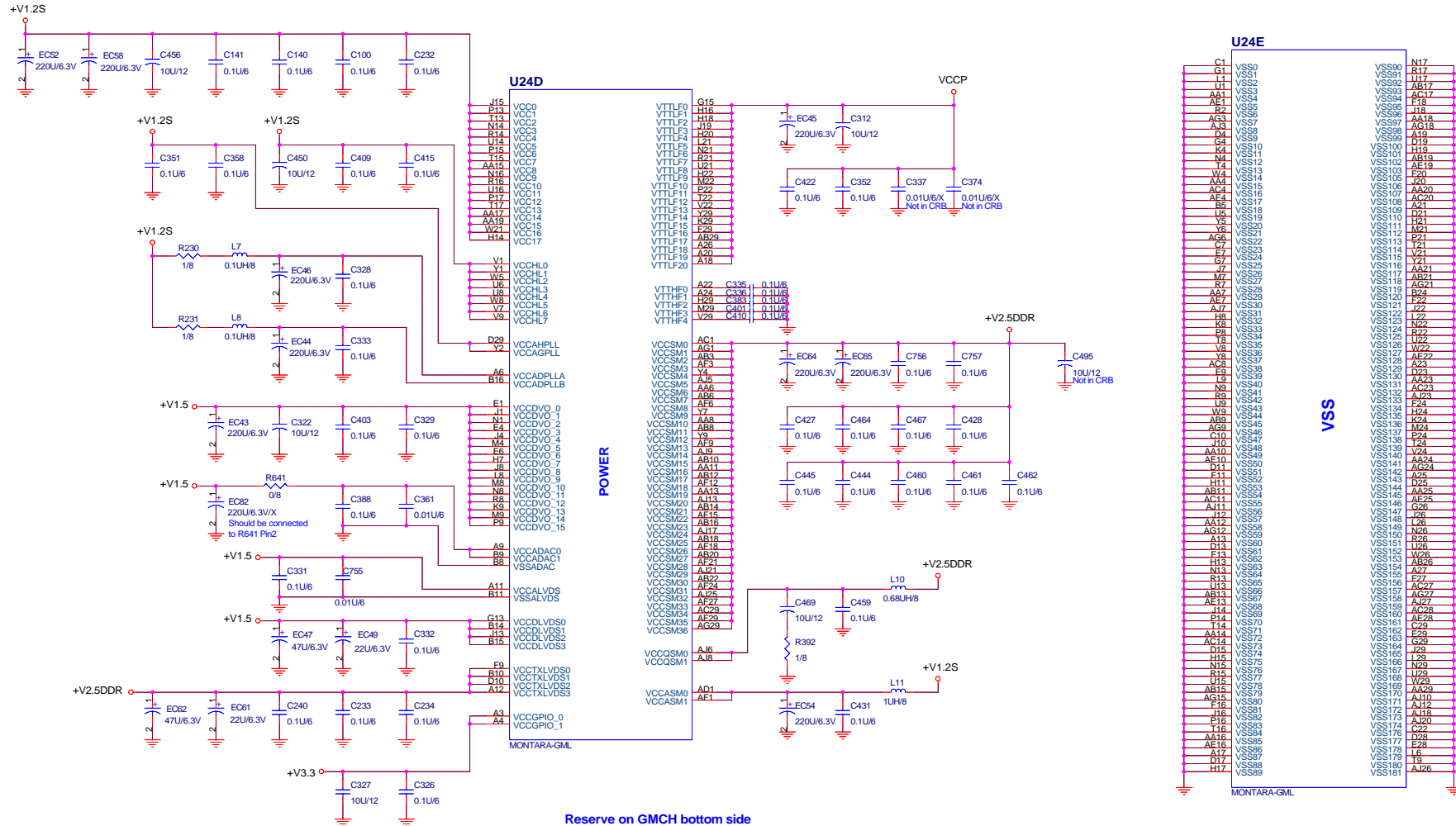


Near LVDS CONN.

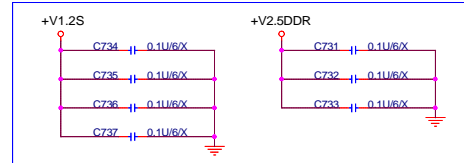


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852GME_C

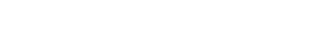
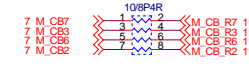
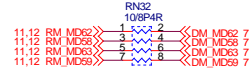
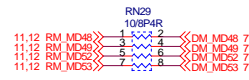
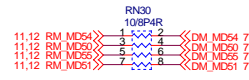
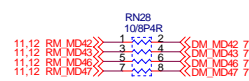
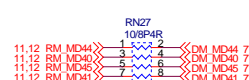
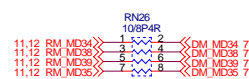
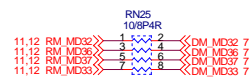
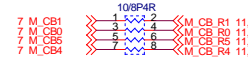
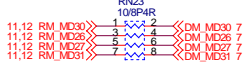
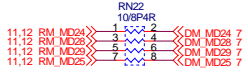
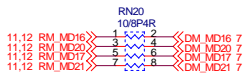
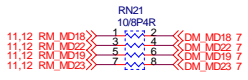
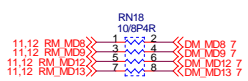
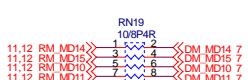
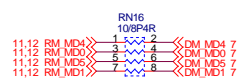
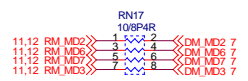


Reserve on GMCH bottom side

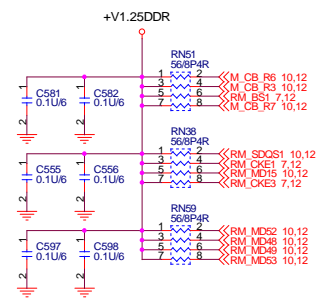


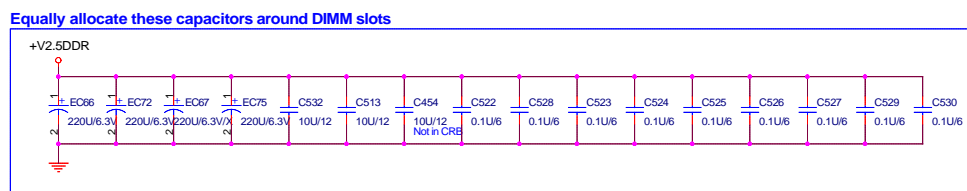
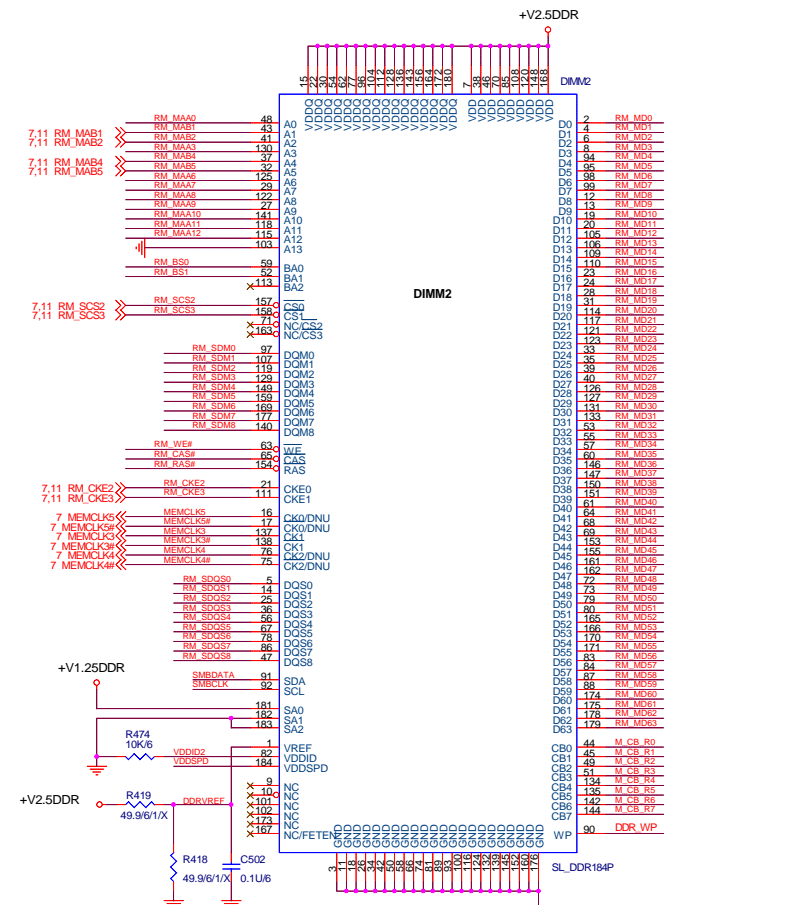
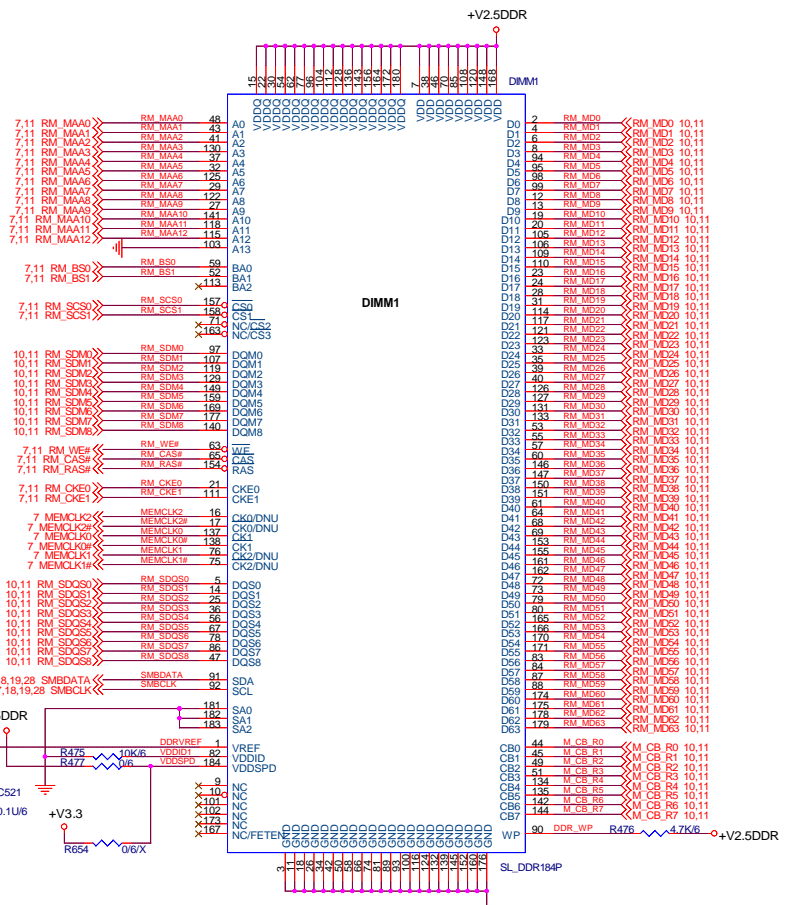
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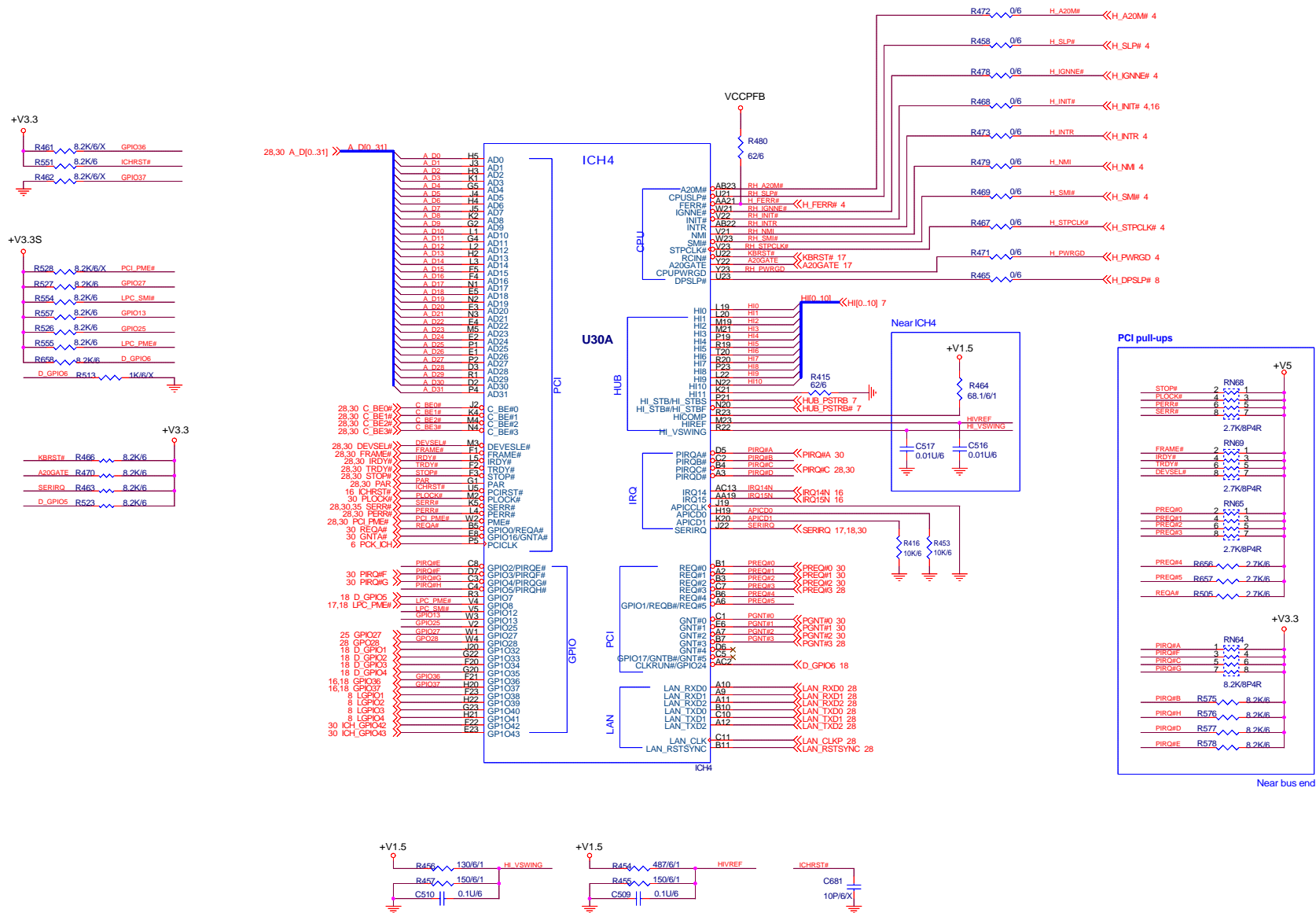
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Equally allocate these capacitors around DIMM slots

82801DB_1



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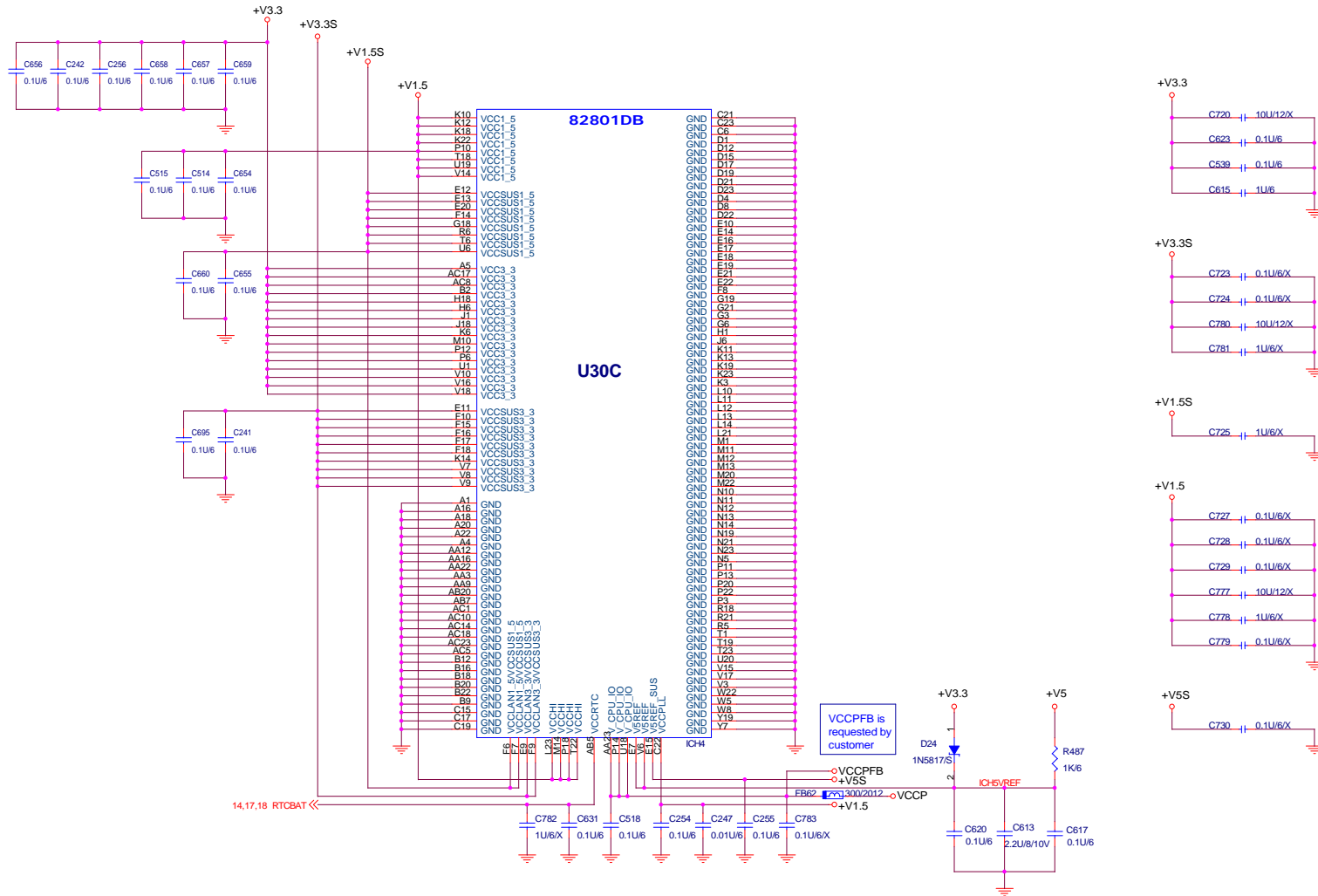
82801DB_2



J19	Function
Open	Case Closed
Short	Case Opened

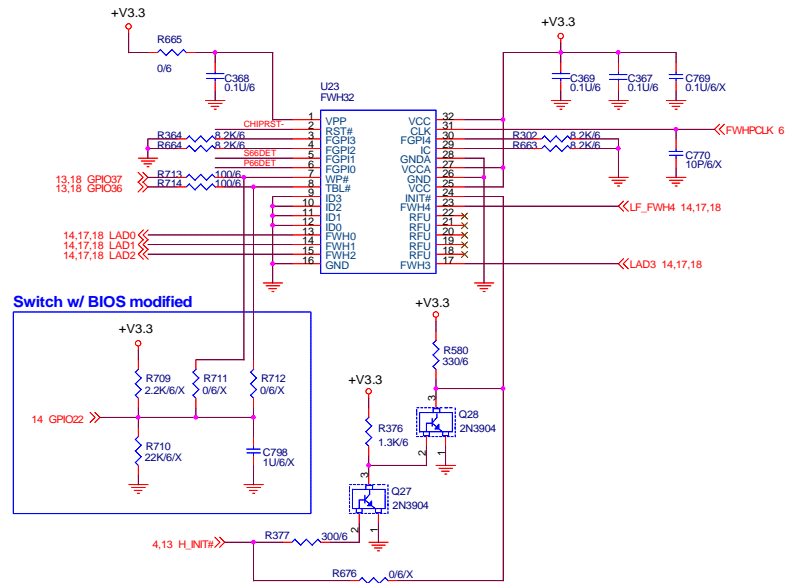
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82801DB_3

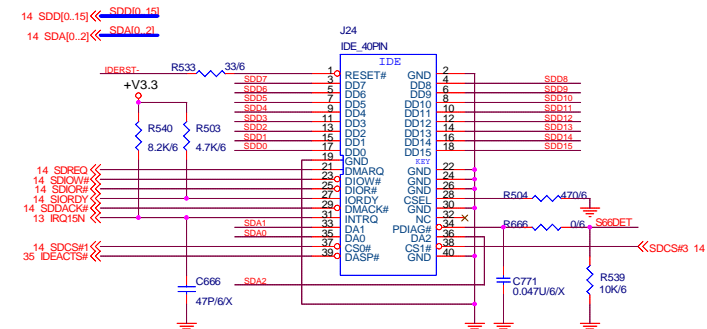
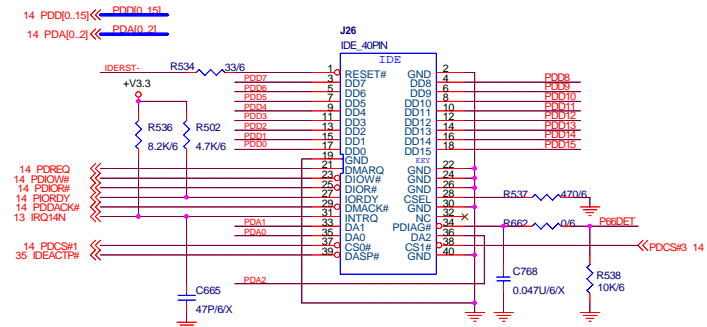
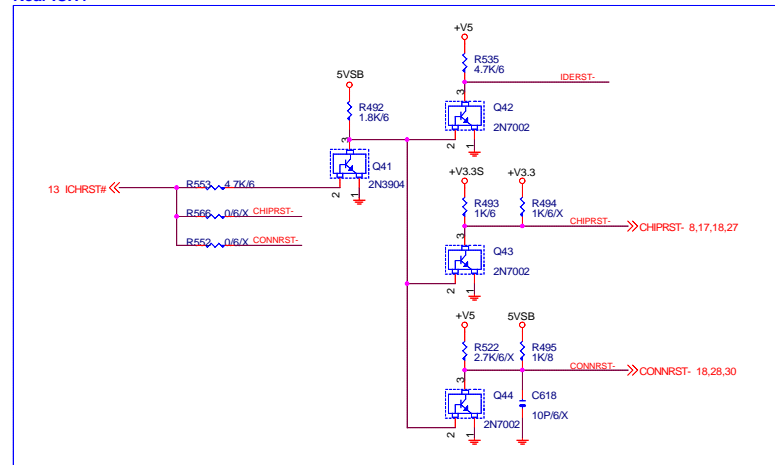


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FirmWare Hub (FWH) Socket

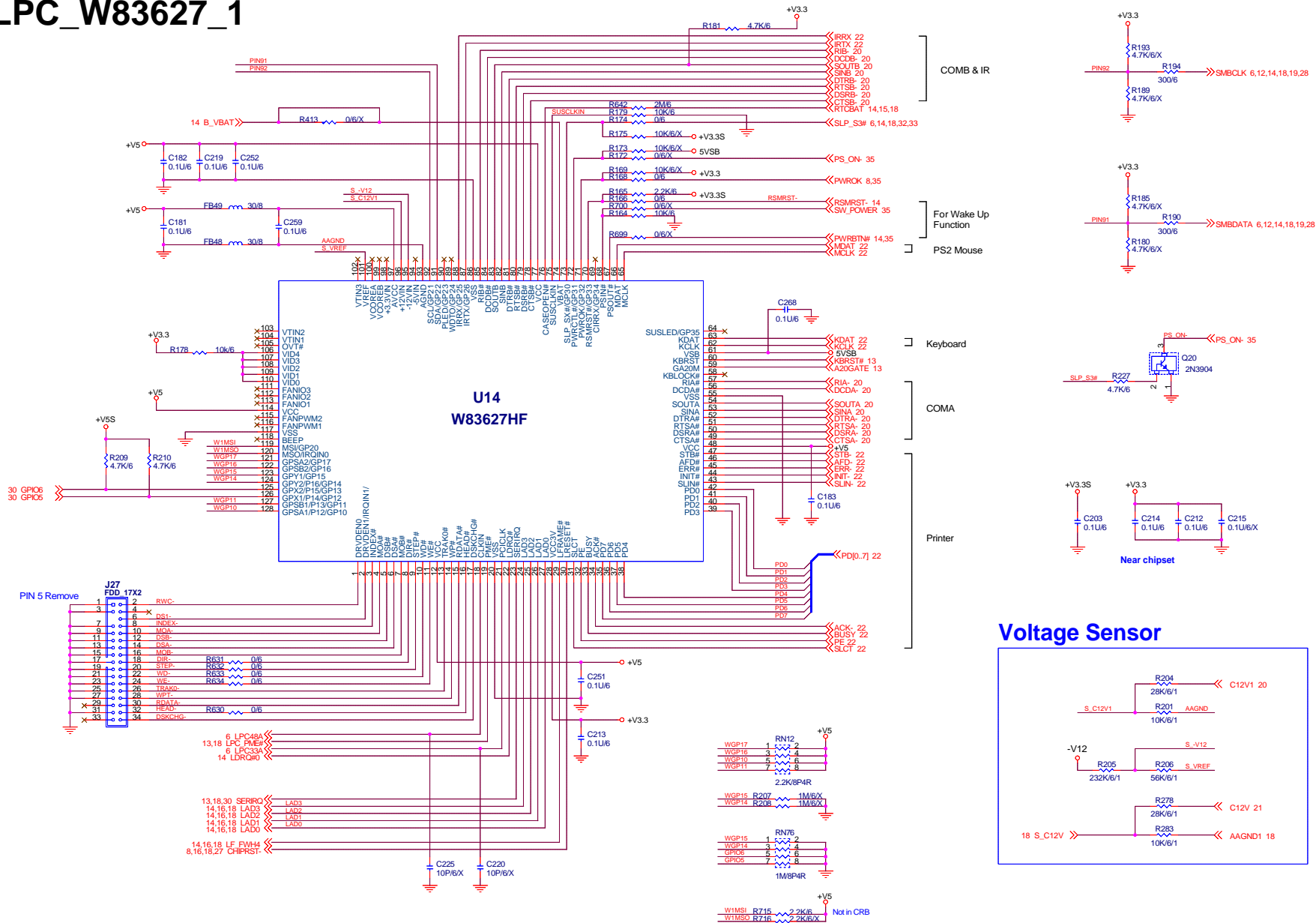


Near ICH4



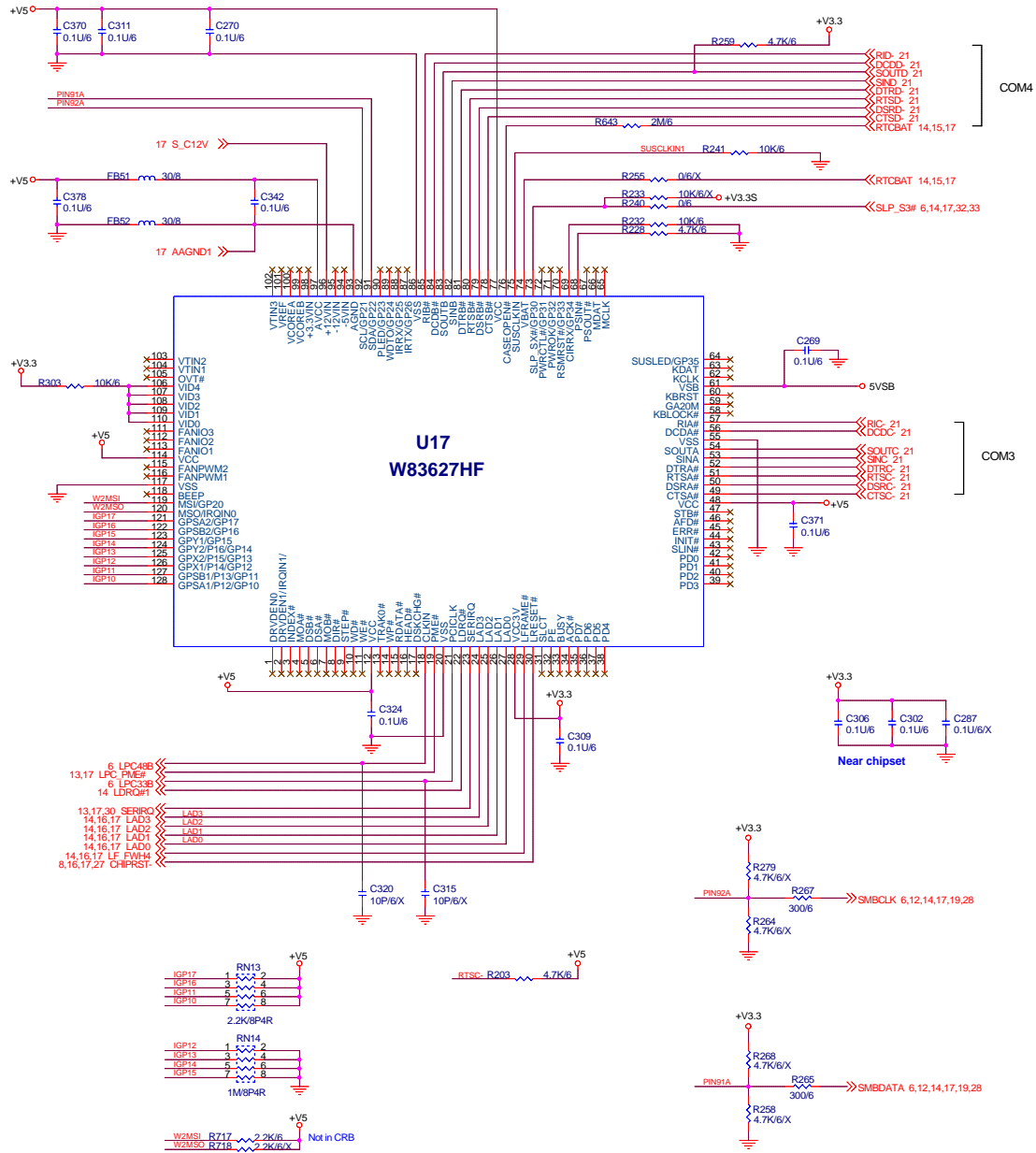
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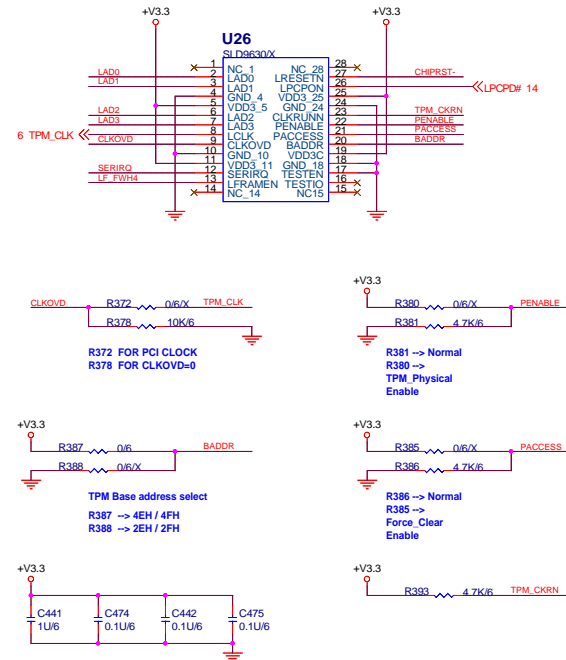


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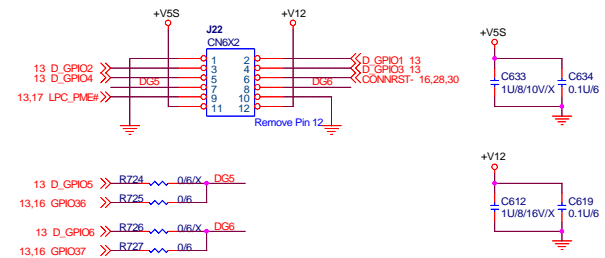
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TPM

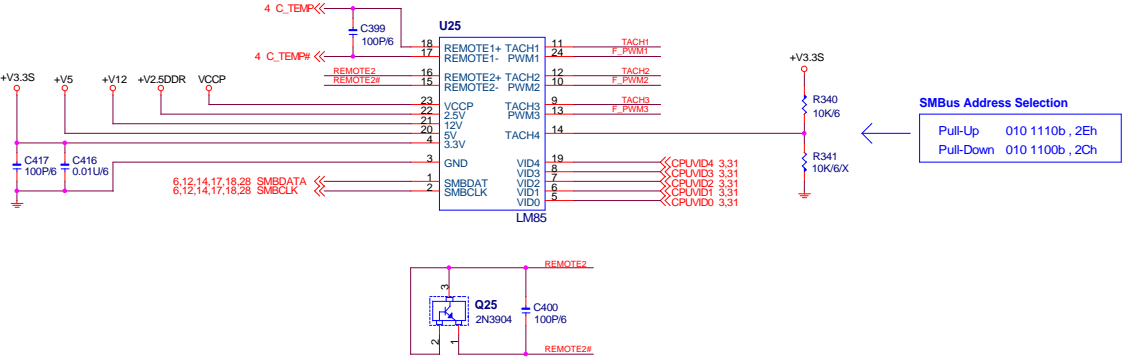


Daughter Card Interface

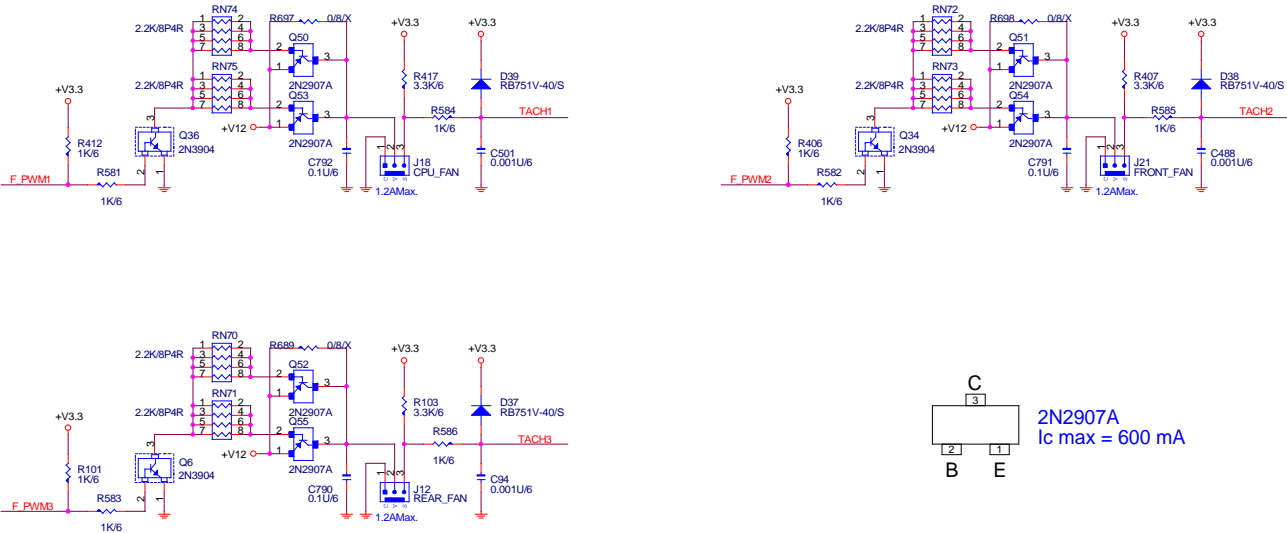


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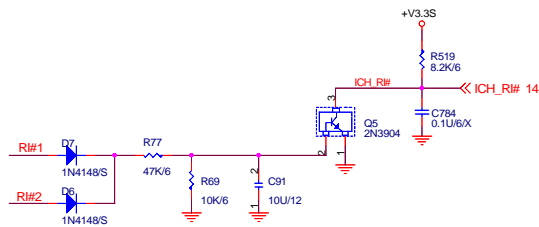
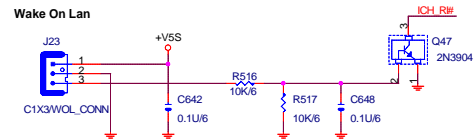
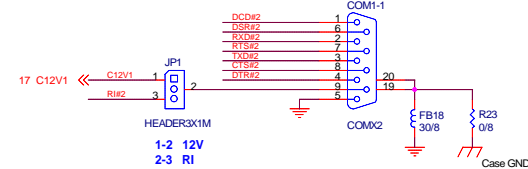
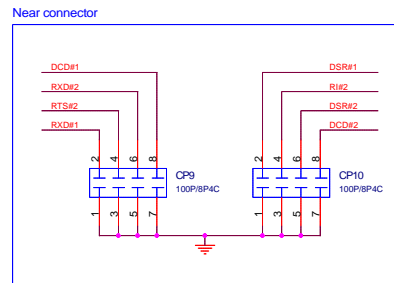
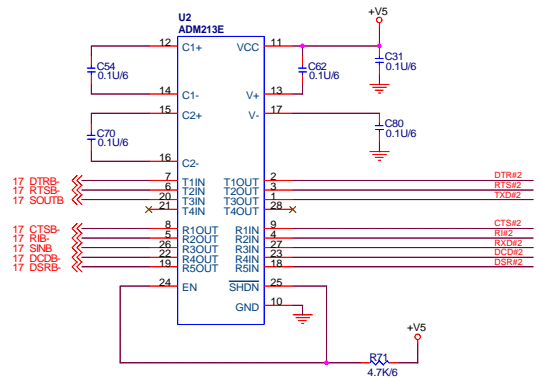
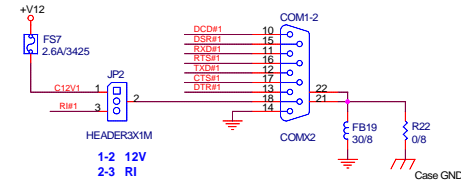
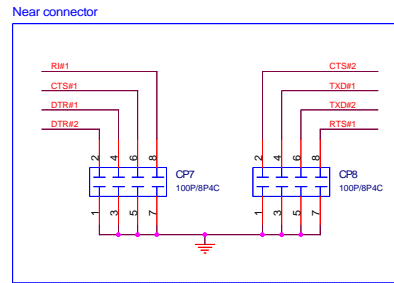
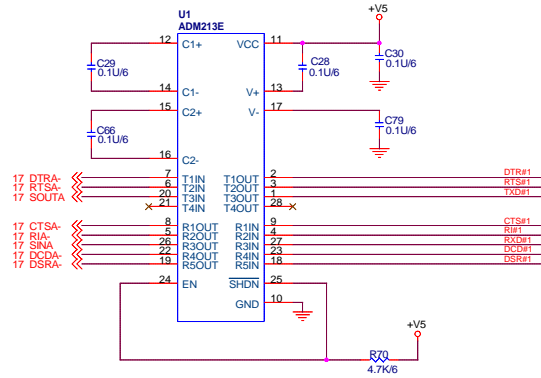
HARDWARE MONITOR



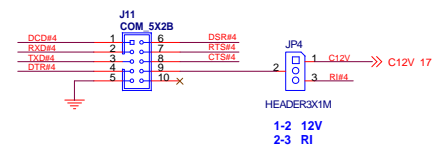
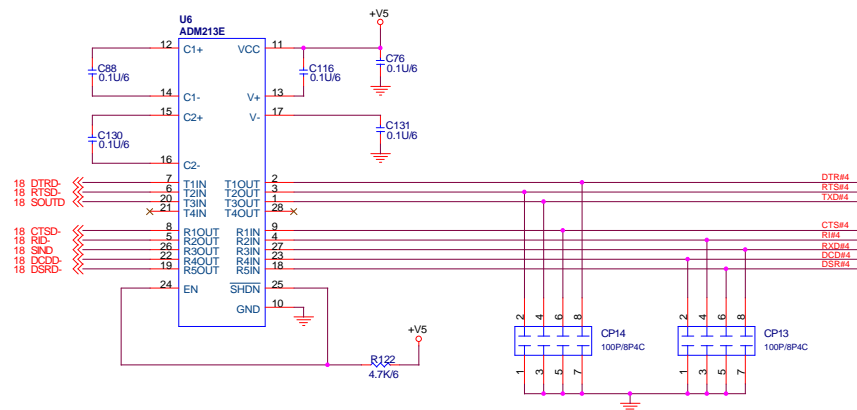
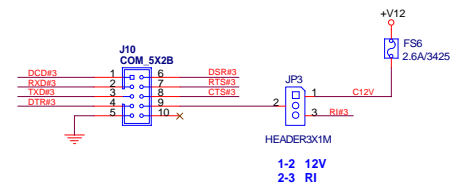
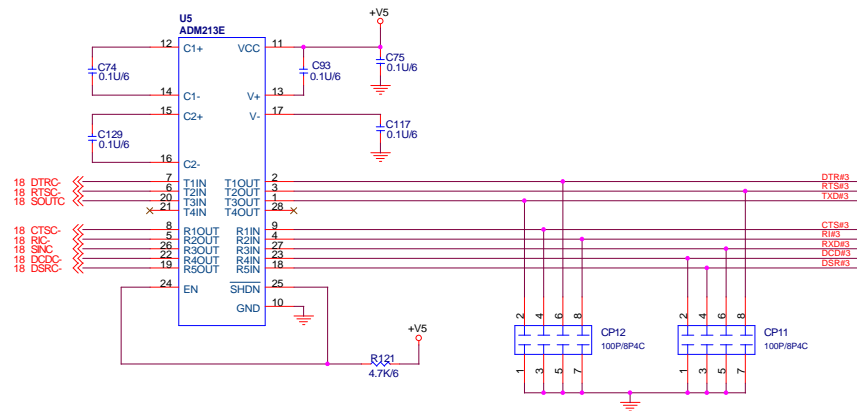
FAN CONN.



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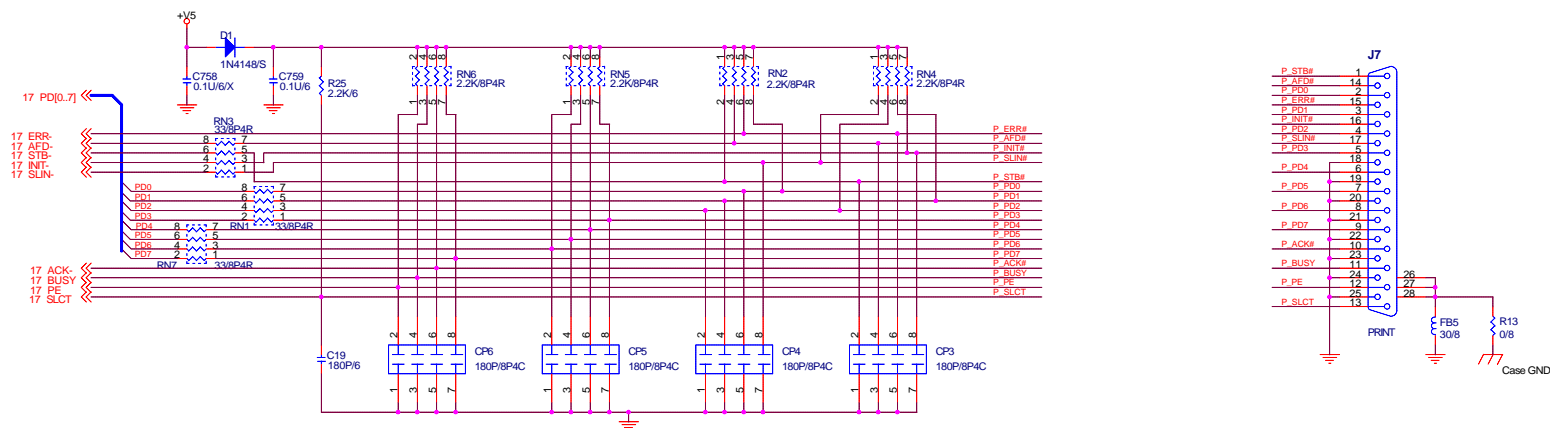


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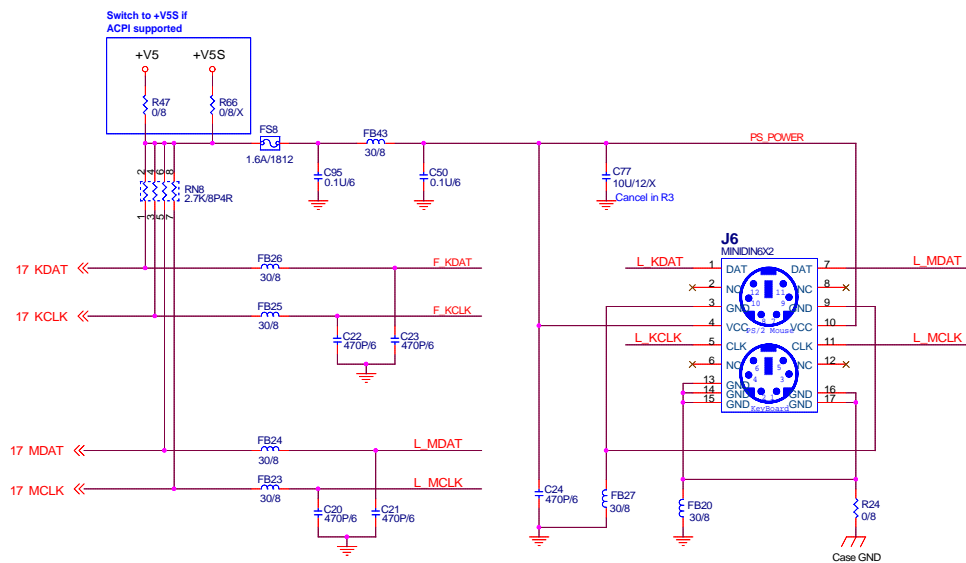


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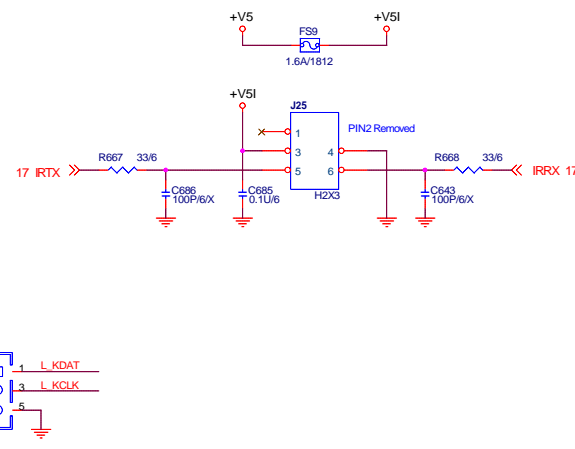
Parallel Port Connector



K/B & M/S

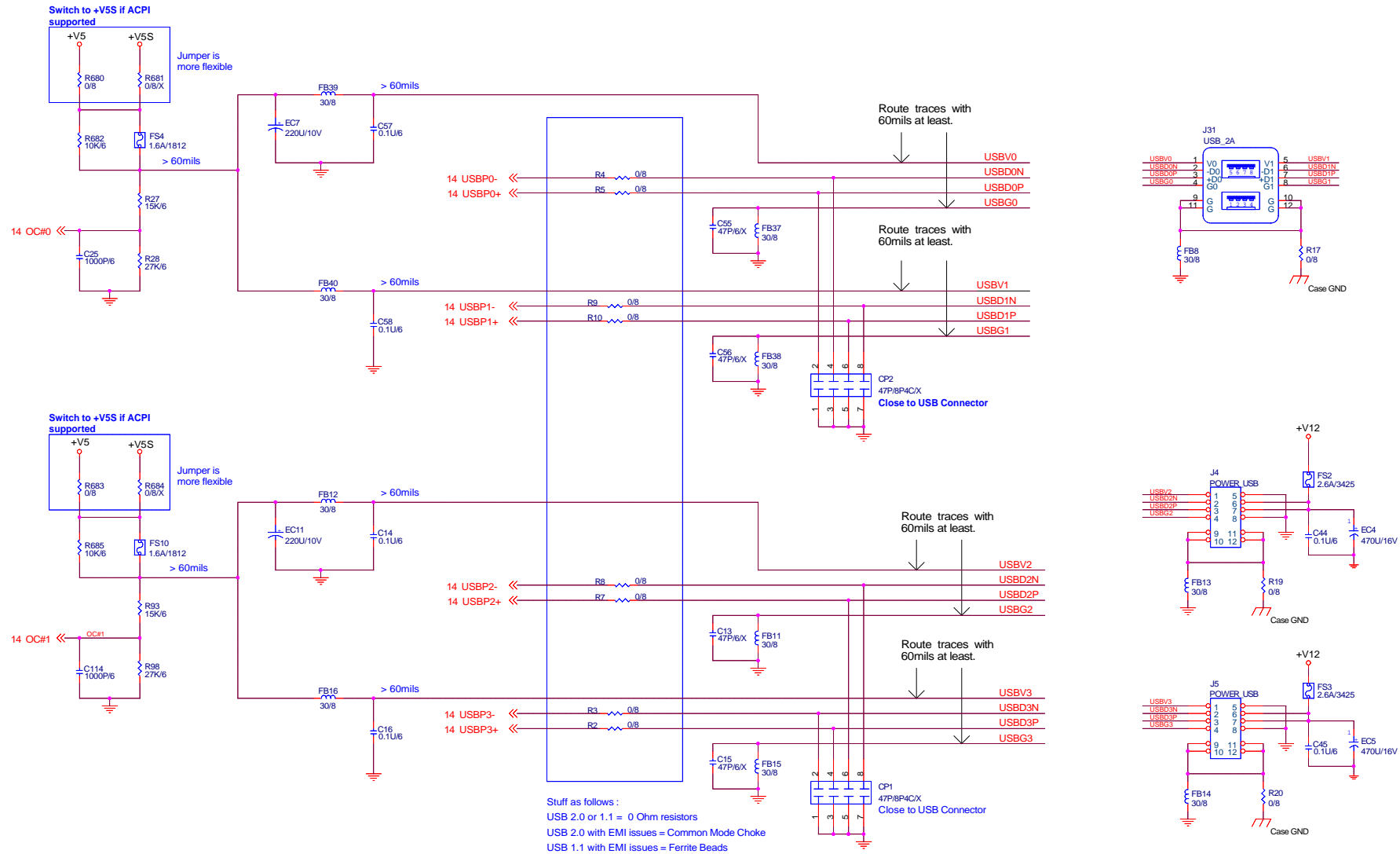


IR Connector



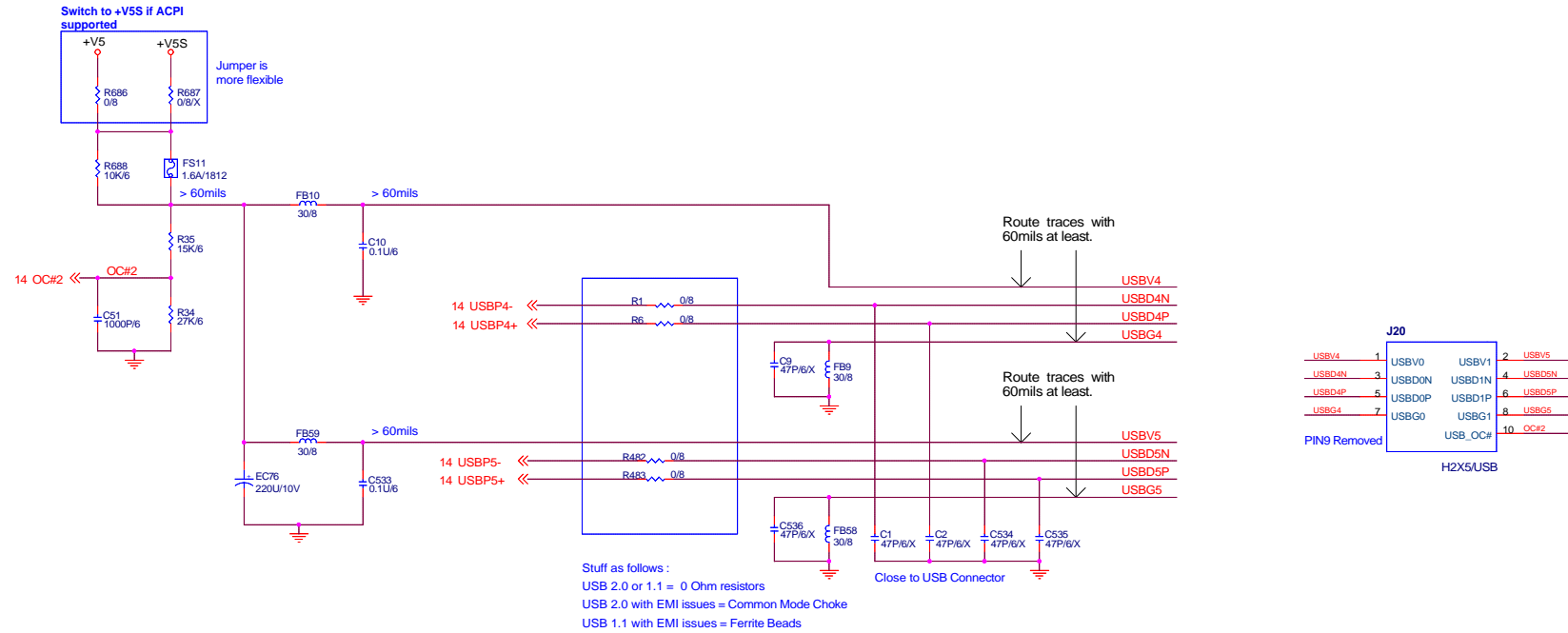
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USB_1-4



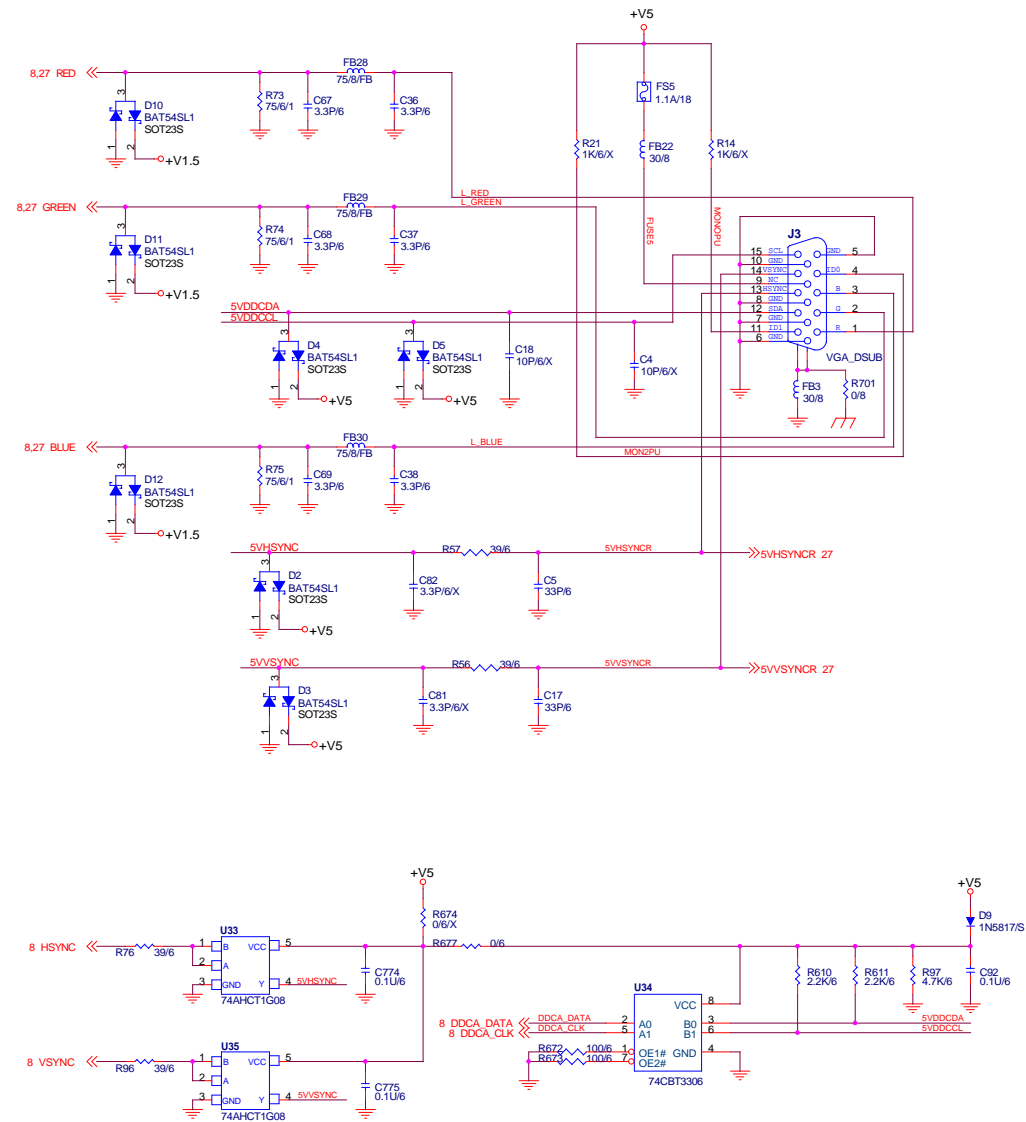
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USB_5-6



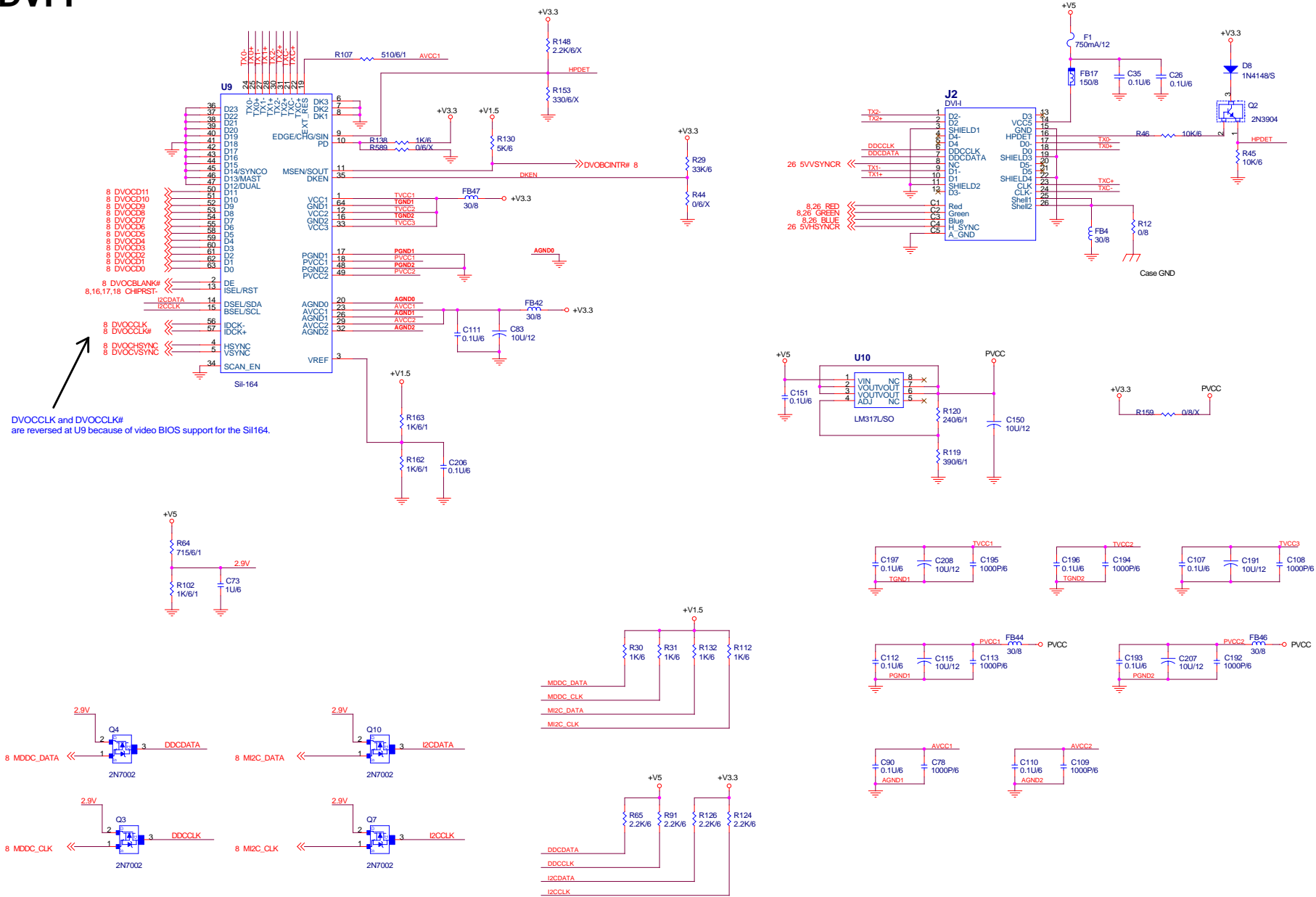
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CRT CONN.

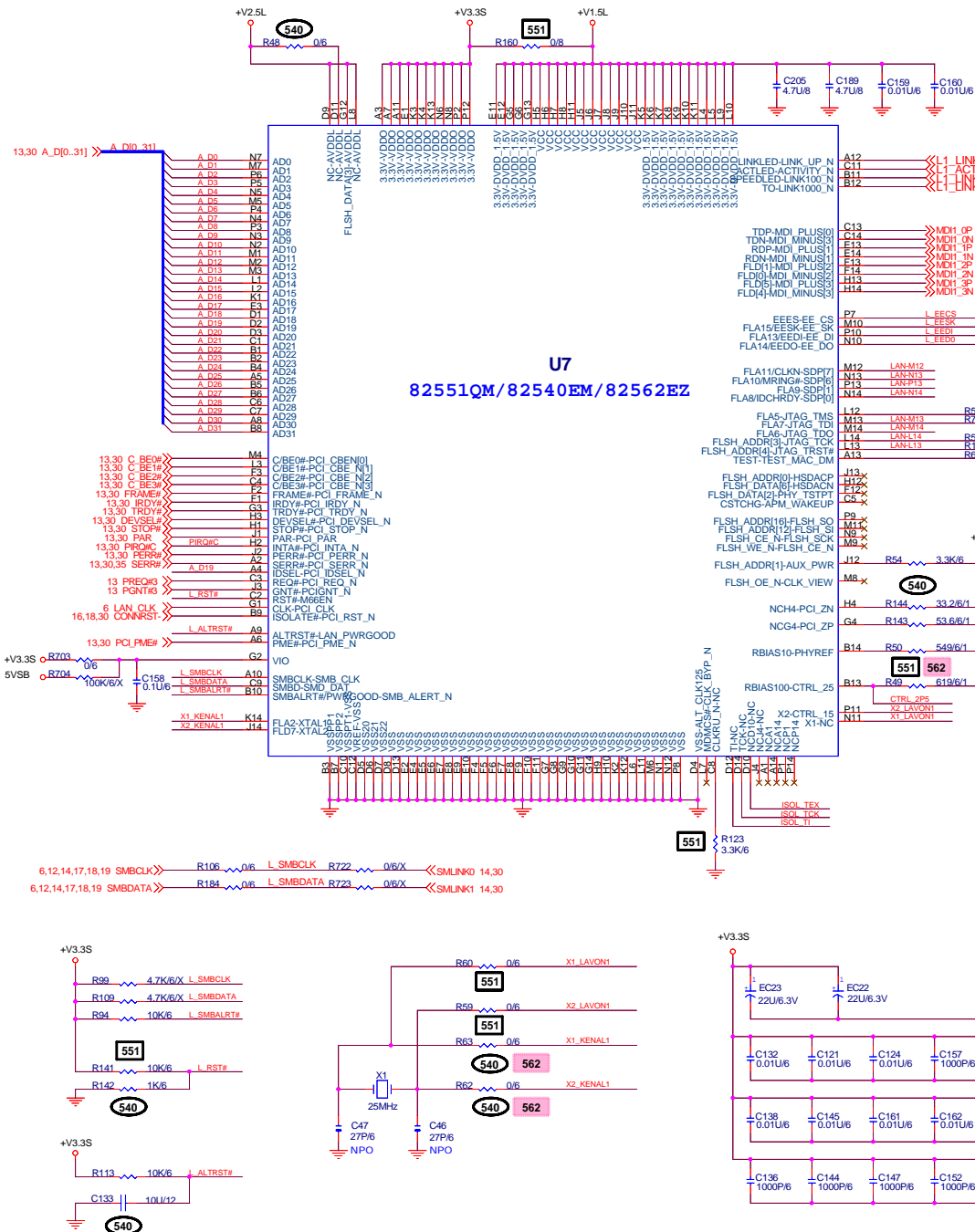


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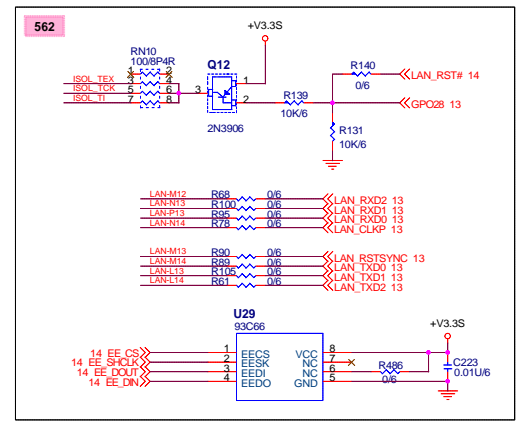
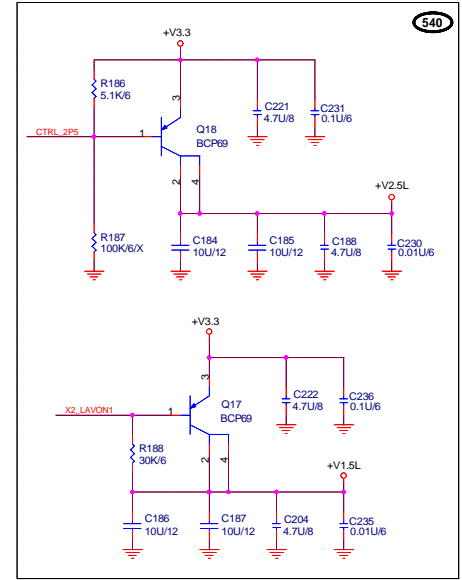
DVI-I



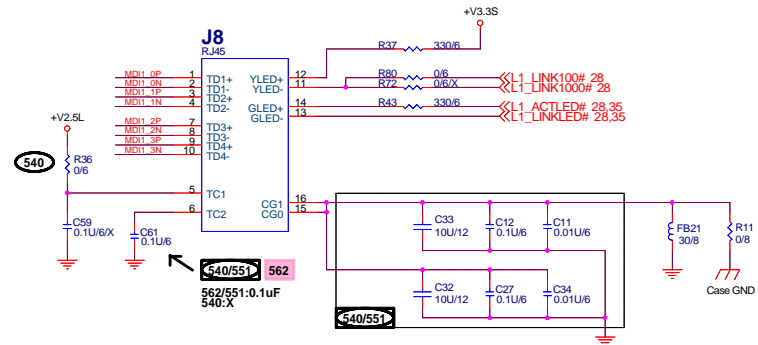
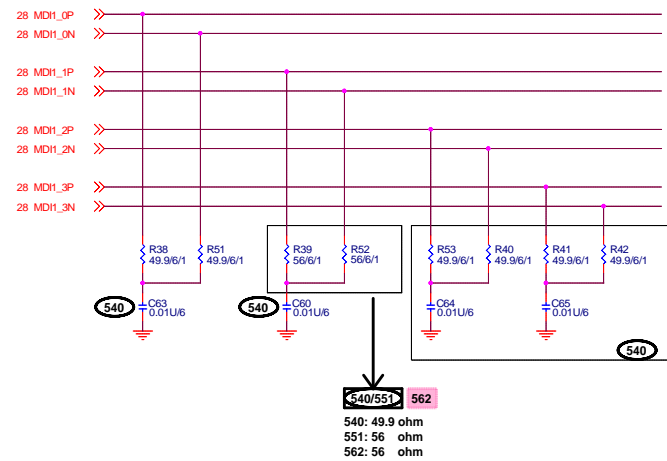
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- 562** : Populate component if using 82562EZ
- 551** : Populate component if using 82551
- 540** : Populate component if using 82540
- 540/551** : Populate different component depending on 82551 or 82540.



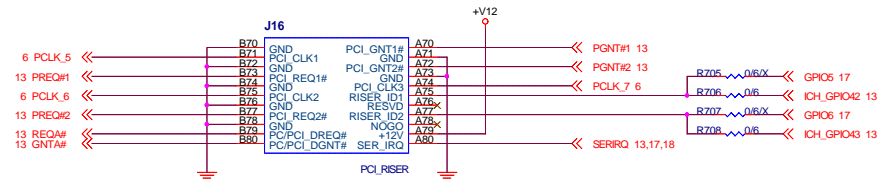
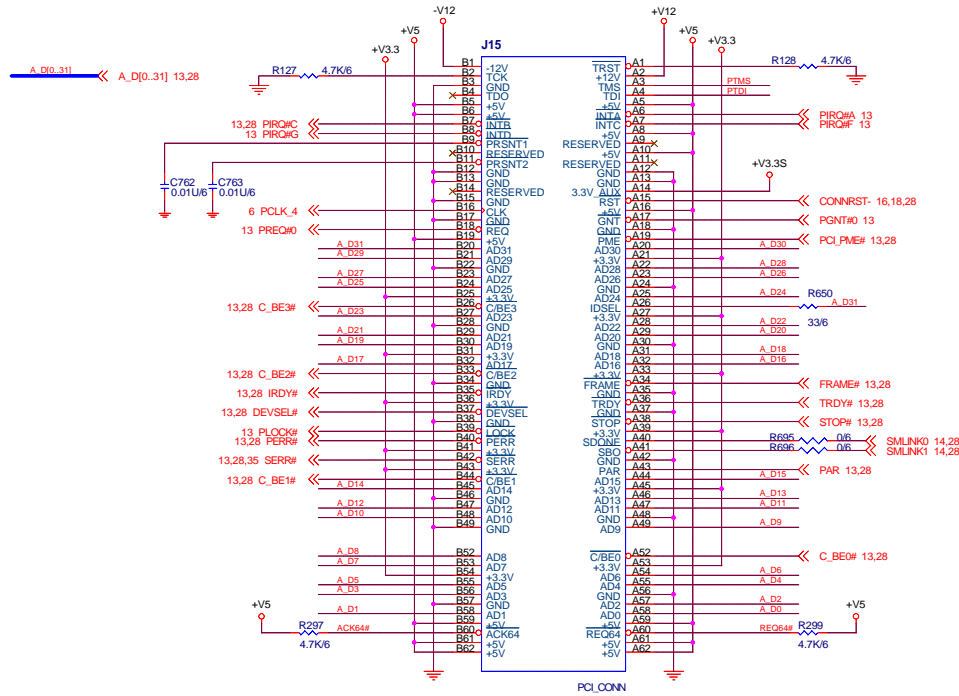
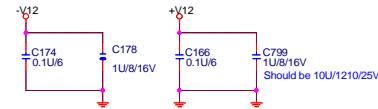
LAN CONN.



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PCI RISER

Equally allocate these capacitors among related power planes equally



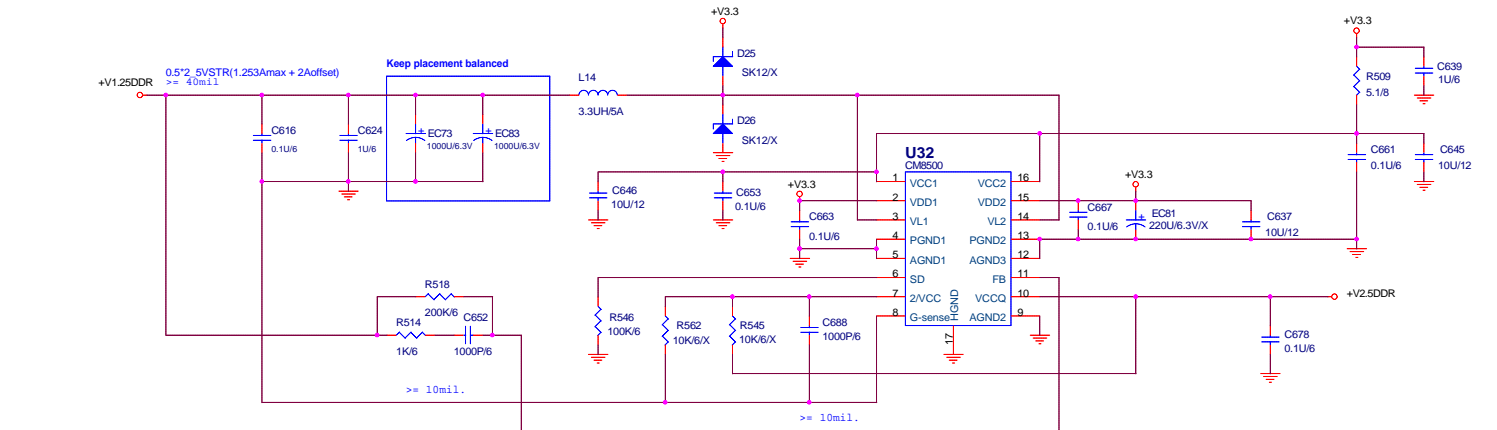
Slot 1: AD31 , A.C.F.G, REQ#0, GNT#0
Slot 2: AD29 , C.F.G.A, REQ#1, GNT#1
Slot 3: AD27 , F.G.A.C, REQ#2, GNT#2

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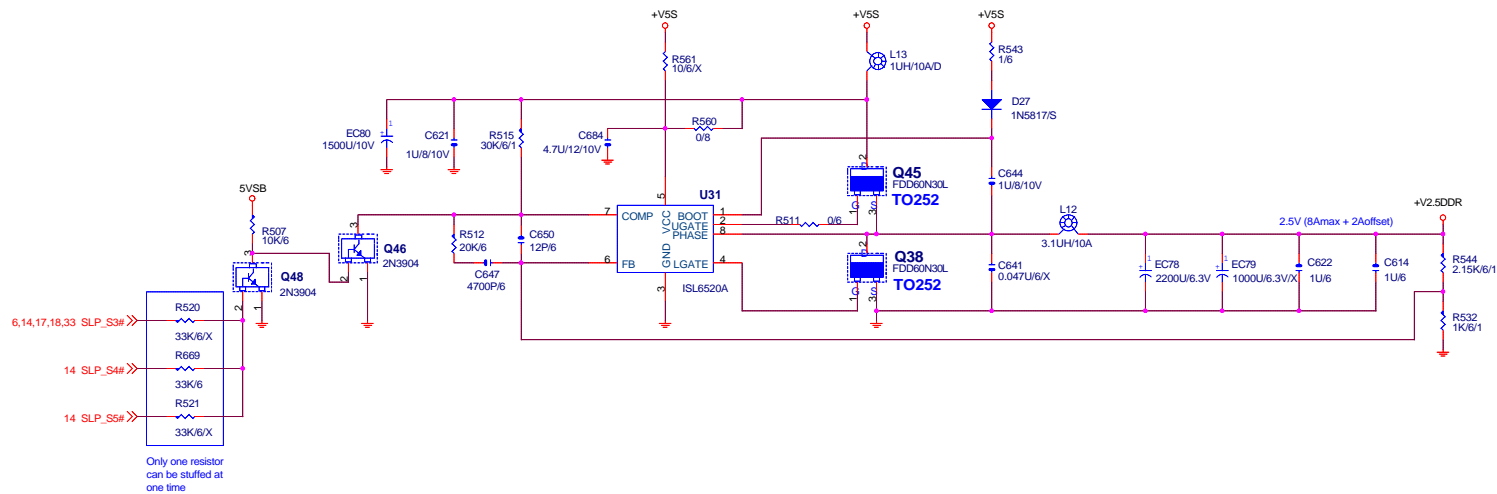
CPU POWER VRM 10.0

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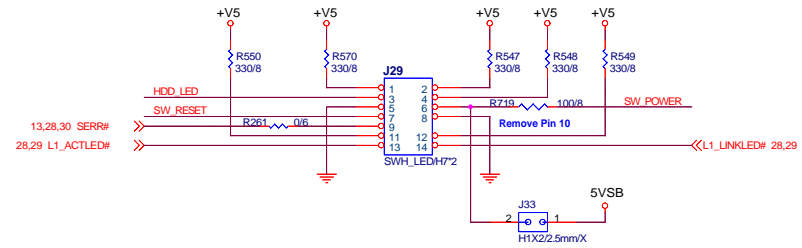
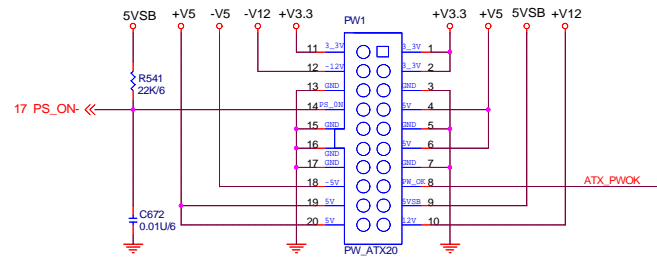
1.25V DDR Termination Voltage



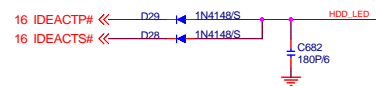
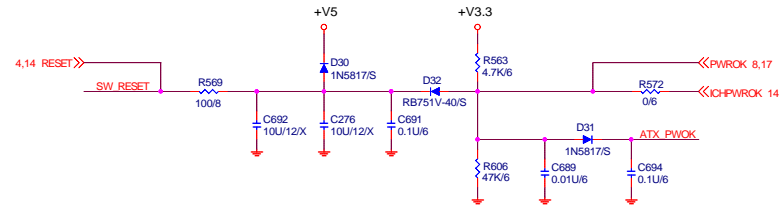
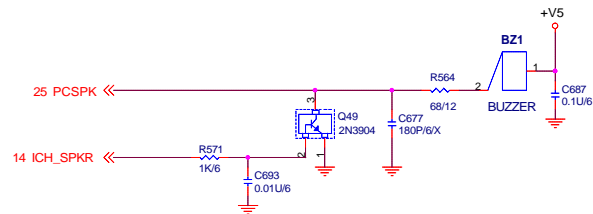
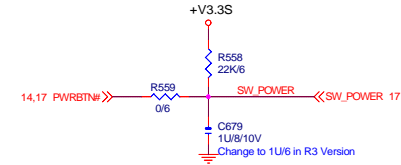
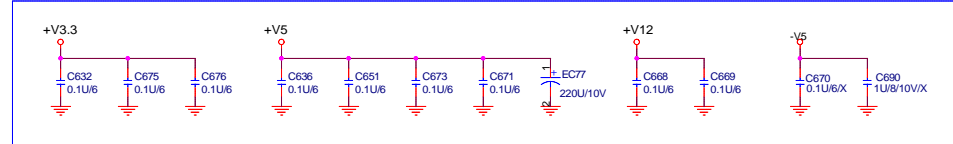
All the high-frequency decoupling capacitors should be allocated very closely to the main chip (CM8500), especially for the two capacitors at Pin 1 & Pin 16.



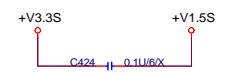
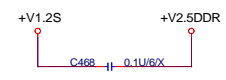
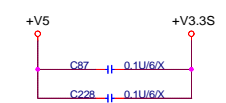
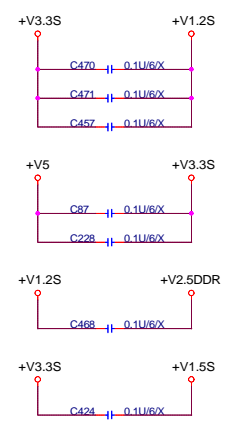
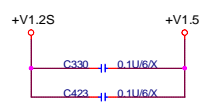
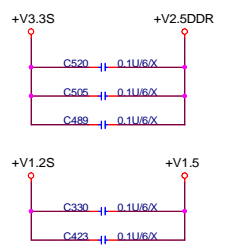
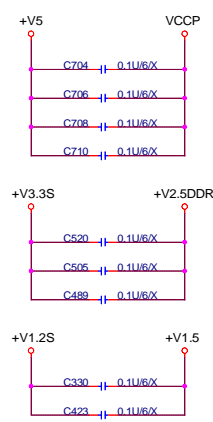
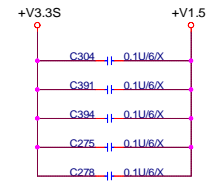
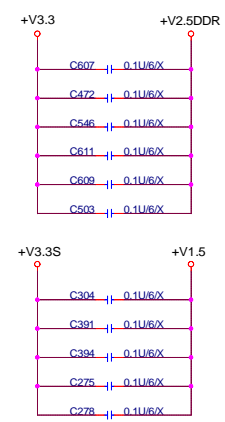
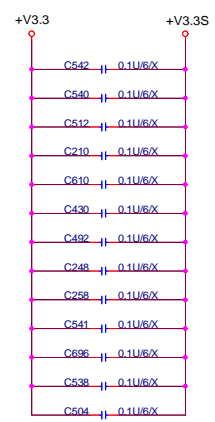
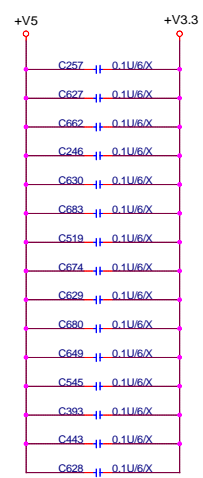
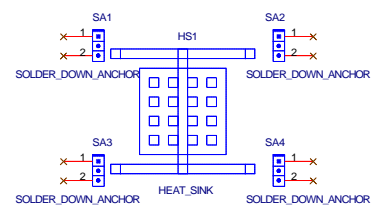
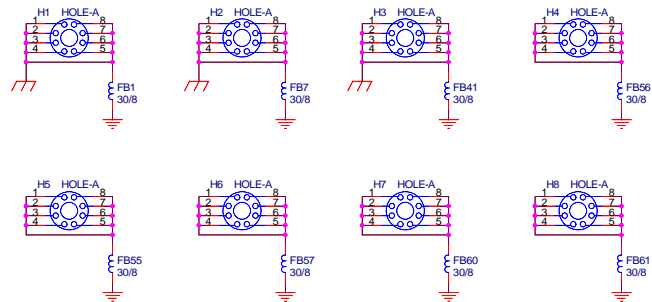
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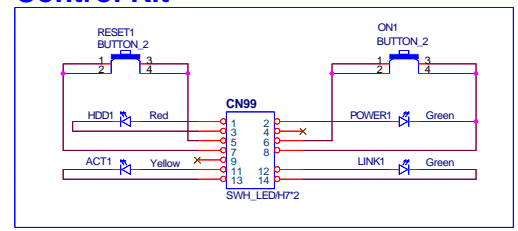
Near ATX Connector



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Control Kit



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